

Class Subclass  
ISSUE CLASSIFICATION  
08/160909

UTILITY  
SERIAL  
NUMBER 08/160909

PATENT DATE

PATENT  
NUMBER

SERIAL NUMBER  
08/160,909

FILING DATE  
12/03/93

CLASS  
~~135~~  
117

SUBCLASS  
84  
~~601~~

GROUP ART UNIT  
1107

EXAMINER  
K. WILCOX

APPLICANTS

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\*\*CONTINUING DATA\*\*\*\*\*

VERIFIED

MW

\*\*FOREIGN/PCT APPLICATIONS\*\*\*\*\*

VERIFIED

JAPAN

4-350546

12/04/92

MW

Foreign priority claimed  
35 USC 119 conditions met

☒ yes ☐ no  
☒ yes ☐ no

AS  
FILED

STATE OR  
COUNTRY

SHEETS  
DRWGS.

TOTAL  
CLAIMS

INDEP.  
CLAIMS

FILING FEE  
RECEIVED

ATTORNEY'S  
DOCKET NO.

Verified and Acknowledged

Examiner's Initials

JPX

5

20

3

\$710.00

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ADDRESS

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TITLE

METHOD OF FABRICATING SEMICONDUCTOR DEVICES AND APPARATUS FOR  
PROCESSING A SEMICONDUCTOR

U.S. DEPT. of COMM. Pat. & TM Office — PTO-436L (rev. 10-78)

PARTS OF APPLICATION  
FILED SEPARATELY

Applications Examiner

NOTICE OF ALLOWANCE MAILED

CLAIMS ALLOWED

Total Claims

Print Claim

Assistant Examiner

ISSUE FEE

Amount Due

Date Paid

DRAWING

Sheets Drwg.

Figs. Drwg.

Print Fig.

Label  
Area

Primary Examiner

ISSUE  
BATCH  
NUMBER

PREPARED FOR ISSUE

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